



**Webinar on “Low-Power Design Techniques for Digital Circuits”
IEEE Circuits and Systems (CAS) Society Bangalore Chapter**

in Association with
IEEE Bangalore Section

CERTIFICATE OF PARTICIPATION

is hereby presented to

Varuna Kumara

In appreciation for participation in the **Webinar on “*Low-Power Design Techniques for Digital Circuits*”** organized by IEEE Circuits and Systems (CAS) Society Bangalore Chapter in association with IEEE Bangalore Section held on **26th June, 2020**.

Dr. Devesh Dwivedi

Chair, CAS Bangalore Chapter

Mr. Puneet Kumar Mishra

Chair, IEEE Bangalore Section