



Moodlakatte Institute of Technology

(A Unit of Moodlakatte Nagarathna Bhujanga Shetty Trust (R.))

(Approved by AICTE, New Delhi & Affiliated to VTU, Belagavi)

Moodlakatte – 576 217, Kundapura Taluk, Udupi District, Karnataka

Date: 12.03.2023

Topic : **Hands on Research oriented workshop on “VLSI system design”**

Organized On : **11th March 2023**

Resource Person : 1. Dilipa Mogaveera, Director, KarMic Design pvt.Ltd
2. Rizwan Roshan, Program Manager Cyient Semiconductor BU

Contact :
1. <https://www.linkedin.com/in/dilipa-mogaveera-85932719>
2. https://in.linkedin.com/in/rizwan-roshan-4768792a?trk=people-guest_people_search-card

Participants : **Third Year and Final Year Students of E&C**

No. of Student – 49.

Faculty- 27

PREAMBLE

In today's world one cannot imagine without electronic devices and Gadgets. Every electronic Gadget is the outcome of revolution in VLSI technology. The department of ECE has undertaken the initiative of conducting workshop on VLSI System design.

One day hands on Research oriented workshop on “VLSI system design” held to students for give the knowledge about Semiconductor company opportunities, how chips are designed in companies and basics of Semiconductor technology.

BRIEF PROFILE OF THE CHIEF GUEST

Rizwan Roshan is a Program Manager in Cyient Semiconductor BU. He is a seasoned professional with more than 13 years of experience in Semiconductors, VLSI, ASIC standard cell library development.

REPORT

A one-day technical research-oriented workshop was organized on the subject of VLSI System Design by the Department of Electronics and Communication. Rizwan Roshan, Program Manager of Cient Semiconductor and Dilip Mogaveera, Director of Karmic Design Pvt Ltd, were the resource person and conducted a workshop for the students. After inaugurating the workshop, he explained the importance of semiconductor technology, how to make chips, opportunities in semiconductor companies. Principal of the college, Dr. Abdul Karim, Vice Principal Prof. Melvin de Souza, Head of the Department Professor Balanageshwar, Head of the Department of Artificial Intelligence Professor Varunakumar, staff, students were present. Professor Sushma Shetty welcomed, student Sinchana Devadiga delivered vote of thanks, Fatima narrated.

FEEDBACK

Resource Person: The hand on workshop was well planned and properly executed. The response from students was good. Students raised many questions during the workshop. The Resource Person was particularly motivated by the interest aroused in the students during the workshop.

Student Feedback: Gained knowledge of VLSI Technology and its benefits and opportunities. Session was interesting and informative. Students felt the resource person was Knowledgeable and they raised many questions.

Faculty Feedback: The resource person was knowledgeable and informative. The faculty suggested conducting more such workshops in future.

ORGANIZING COMMITTEE

Chair:

Mr. Balanageshwar, HOD Dept of ECE

Staff co-coordinators

Mrs. Sookshma Adiga

Assistant professor(Sr.), Dept of CSE

Mrs. Sushma Shetty

Assistant Professor, Dept of ECE

Student Coordinators:

Mr. Fathima 3rd Sem ECE

Mr. Sinchana Devadiga 3rd Sem ECE

Poster



MOODLAKATTE INSTITUTE OF TECHNOLOGY

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Hands-on Workshop on "VLSI SYSTEM DESIGN"

organized by

Department of Electronics & Communication Engineering

Resource Person



Dilipa Mogaveera

Director,
KarMic Design Pvt Ltd



Rizwan Roshan

Program Manager,
Cyient Semiconductor BU

Date : 11/03/2023

Date : 10:00 AM

Venue : Auditorium



mitkundapura

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Photos

ವಿಜಯ ಕರ್ನಾಟಕ = 20/03/2023


ಮೂಡ್ಲಕಟ್ಟೆ: ತಾಂತ್ರಿಕ ಕಾರ್ಯಾಗಾರ



ಕುಂದಾಪುರ: ಮೂಡ್ಲಕಟ್ಟೆ ಎಂಜಿನಿಯರಿಂಗ್ ಕಾಲೇಜಿನ ಎಲೆಕ್ಟ್ರಾನಿಕ್ಸ್ ಆಂಡ್ ಕಮ್ಯುನಿಕೇಶನ್ ವಿಭಾಗದ ವತಿಯಿಂದ 'ಎಎಲ್‌ಎಸ್‌ಐ ಸಿಸ್ಟಂ ವಿನ್ಯಾಸ' ವಿಷಯದ ಬಗ್ಗೆ ತಾಂತ್ರಿಕ ಕಾರ್ಯಾಗಾರ ಜರುಗಿತು. ಸಂಪನ್ಮೂಲ ವ್ಯಕ್ತಿ ಸಿಐಐಟಿ ಸೆಮಿ ಕಂಡಕ್ಟರ್ ಪ್ರೋಗ್ರಾಂ ಮ್ಯಾನೇಜರ್ ರಿಚ್ಚಾರ್ಡ್ ರೋಶನ್, ಕಾರ್ಮಿಕ್ ಡಿಸೈನ್ ವೈ. ಲಿಮಿಟೆಡ್‌ನ ದಿಲೀಪ್ ಮೊಗವೀರ ಸೆಮಿಕಂಡಕ್ಟರ್ ತಂತ್ರಜ್ಞಾನದ ಪ್ರಾಮುಖ್ಯತೆ, ಚಿಪ್ ತಯಾರಿಸುವ ಬಗ್ಗೆ

ಸೆಮಿ ಕಂಡಕ್ಟರ್ ಕಂಪನಿಯಲ್ಲಿನ ಅವಕಾಶ ಕುರಿತು ವಿವರ ನೀಡಿದರು. ಪ್ರಿನ್ಸಿಪಾಲ್ ಡಾ.ಅಬ್ದುಲ್ ಕರೀಂ, ವೈಸ್ ಪ್ರಿನ್ಸಿಪಾಲ್ ಪ್ರೊ.ಮೆಲ್ವಿನ್ ಡಿಸೋಜಾ, ವಿಭಾಗದ ಮುಖ್ಯಸ್ಥ ಪ್ರೊ. ಬಾಲನಾಗೇಶ್ವರ್, ಆರ್ಟ್ ಫಿಷಿಯಲ್ ಇಂಟಿಲಿಜೆನ್ಸ್ ವಿಭಾಗದ ಮುಖ್ಯಸ್ಥ ಪ್ರೊ. ವರುಣ್ ಕುಮಾರ್ ಉಪಸ್ಥಿತರಿದ್ದರು. ಪ್ರಾಧ್ಯಾಪಕಿ ಸುಷ್ಮಾ ಶೆಟ್ಟಿ ಸ್ವಾಗತಿಸಿದರು. ಫಾತಿಮಾ ಕಾರ್ಯಕ್ರಮ ನಿರ್ವಹಿಸಿದರು. ವಿದ್ಯಾರ್ಥಿನಿ ಸಿಂಚನೆ ದೇವಾಡಿಗ ವಂದಿಸಿದರು.


HOD


Principal



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Date: 8.3.2023


CIRCULAR

This is to inform all Faculties, and third year and final year students of the Department of Electronics & Communication Engineering that a Hands-on Research oriented Workshop on VLSI Design will be organised on 11th March 2023 in the Auditorium at 10AM. Eminent Persons from Industry would be the resource person.

All are requested to participate in this workshop.


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To:  circulate among staff and students

Copy to: Department File



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Hands on workshop on “VLSI System Design” on 11.3.2023		
Participant List		
Sl.no	Name	USN
1	ANUP SIDDAPPA	4MK20EC001
2	ASHRITHA R SHETTY	4MK20EC002
3	CHAITHRA R	4MK20EC003
4	CHANDANA R	4MK20EC004
5	DEELAKSHA V H	4MK20EC005
6	GNANIKA R	4MK20EC006
7	HAFIZ ABUMOHAMMED BYARI	4MK20EC007
8	IRFAN A SHAIKH	4MK20EC008
9	KOUSHIK B S	4MK20EC009
10	MEGHANA	4MK20EC010
11	POOJA V	4MK20EC011
12	PRIYA PATANAKAR	4MK20EC012
13	SANDEEPA D H	4MK20EC013
14	SINCHANA S	4MK20EC014
15	SOWNDARYA S	4MK20EC015
16	VAISHNAVI K	4MK20EC016
17	ABHISHEK BASAPPA PUJARI	4MK21EC001
18	ADAM ISMAIL SHAIKH	4MK21EC002
19	ADARSH K N	4MK21EC003
20	ASHWINI	4MK21EC004
21	BHUMIKA VENKATESH MOGER	4MK21EC005
22	CHETAN SHRINIVAS NAIK	4MK21EC006
23	CHIDROOP C S	4MK21EC007
24	DEEKSHITA SURESH NAIK	4MK21EC008
25	FATIMA TAHSIR	4MK21EC009
26	GOUTHAMI	4MK21EC011
27	JEEVAN M K	4MK21EC012
28	KAVYA SHIVARAM GOWDA	4MK21EC013
29	N K VAISHNAVI	4MK21EC014
30	NAGESH VISHNUMOORTHY HEBBAR	4MK21EC015
31	NIKHITHA	4MK21EC016
32	NISHA NARAYAN MOGER	4MK21EC017
33	PUSHPA PUNYA	4MK21EC018
34	RAJATH ACHAR	4MK21EC019
35	ROOPA RAJU NAIK	4MK21EC020
36	RUCHITHA KAMATH S	4MK21EC021
37	SHARANYA	4MK21EC022
38	SHASHANKA H D	4MK21EC023
39	SHREERAKSHA R	4MK21EC024
40	SINCHANA B DEVADIGA	4MK21EC026
41	SYED ADIL	4MK21EC027
42	THANUSHREE K G	4MK21EC028

43	TRAPTHI	4MK21EC029
44	VAISHALI SOMANNA GAWADI	4MK21EC030
45	VANDANA S	4MK21EC031
46	VIGNESH	4MK21EC032
47	VINITHA R NAIK	4MK21EC033
48	YASHASWINI JAGADEESH NAIK	4MK21EC034
49	YASHAWANTHA H S	4MK21EC035


HoD


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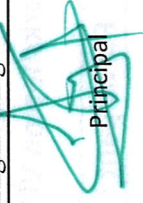
Hands on Research oriented Workshop on VLSI System Design on 11 March 2023

Participant List

Sl.no	Name	Department
1	VARUNA KUMARA	Artificial Intelligence & Machine Learning
2	AKSHATHA SHETTY	Basic Science & Humanities
3	ASHA KUMARI	Basic Science & Humanities
4	BHUNESHWAR PASWAN	Basic Science & Humanities
5	CHAITHRA	Basic Science & Humanities
6	DEEPAK SHETTY M	Basic Science & Humanities
7	NIKHILA PAI	Basic Science & Humanities
8	NIDHISHREE	Civil Engineering
9	DR THILAGARAJ T	Computer Science & Engineering
10	FARANA MOHAMMED IMRAN	Computer Science & Engineering
11	MANOJ KUMAR K	Computer Science & Engineering
12	MURALIDHARA B K	Computer Science & Engineering
13	PRAMEELA	Computer Science & Engineering
14	PRASANNA KUMARA	Computer Science & Engineering
15	SHRINIDHI. N	Computer Science & Engineering
16	SMITHASHREE K	Computer Science & Engineering
17	SOOKSHMA ADIGA	Computer Science & Engineering
18	MAMATHA K S	Electronics & Communication Engineering
19	SUSHMA SHEDTHI A	Electronics & Communication Engineering
20	RAGHUNATHA M D	Electronics & Communication Engineering
21	BALANAGESHWARA S	Electronics & Communication Engineering

22	HARISHA	Master of Business Administration
23	KAVYA	Master of Business Administration
24	VENKATESH SHETTY	Master of Business Administration
25	BASAVARAJ KAMBALE	Mechanical Engineering
26	THIMMAPPA D S	Mechanical Engineering
27	VISHWANATH DINAKAR	Mechanical Engineering


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